



VIMAL JYOTHI ENGINEERING COLLEGE CHEMPERI KANNUR.

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

S5-ECL 333 DIGITAL SIGNAL PROCESSING LABORATORY

Sl. No. LIST OF EXPERIMENTS

- 1. Simulation of Signals**
- 2. Verification of the Properties of DFT**
- 3. Familiarization of DSP Hardware**
- 4. Linear convolution**
- 5. FFT of signals**
- 6. IFFT with FFT**
- 7. FIR low pass filter**
- 8. Overlap Save Block Convolution**
- 9. Overlap Add Block Convolution**