

Vision and Mission of the Department

VISION

To evolve as a centre of excellence, to train students in contemporary technologies, to meet the needs of global industry and to develop them into skillful engineers instilled with human values and professional ethics.

MISSION

To produce competent and disciplined Electrical & Electronics Engineers through delivery of quality education to meet the ongoing global challenges in alignment with technical education system and society.



FAREWELL- 2021-25 BATCH

"AS WE TURN THE PAGE TO A NEW CHAPTER, LET'S CARRY THE MEMORIES, LAUGHTER, AND LESSONS WE'VE SHARED. FAREWELL ISN'T GOODBYE—IT'S A 'SEE YOU LATER' WITH A HEART FULL OF GRATITUDE. WISHING EVERYONE SUCCESS, HAPPINESS, AND ENDLESS ADVENTURES AHEAD!"—ALL THE BEST DEAR STUDENTS.....





FAREWELL- 2021-25 BATCH











FAREWELL- 2021-25 BATCH





STUDENT PUBLICATIONS

 Seminar papers of all final year students are published in EEE Technical magazine- 'Electra 2025'

 All Final year projects are published in International Journal of Scientific Research in Engineering and Management (IJSREM).

PLACEMENTS- CORE COMPANY



MR. ANTONY



MR. SEBIN

SURTI INDUSTRIES, DUBAI



MR. AMAR PRADEEP

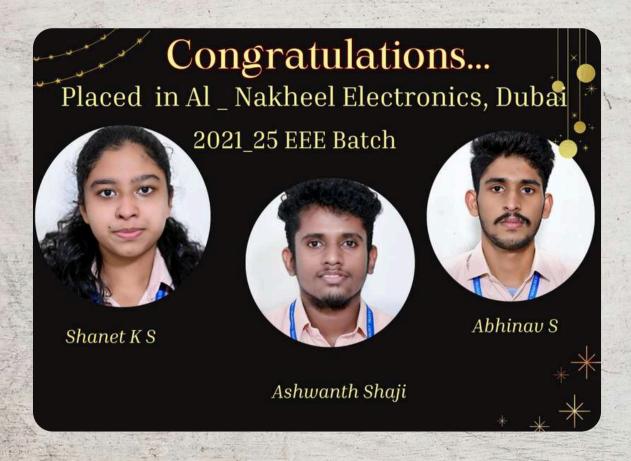


MR. DENO BABY

SEIDOM VEOLIA, ABUDHABI



PLACEMENTS- CORE COMPANY









Shanet K S TEACHNOOK



Amegh K TEACHNOOK



Vinay K K **GRANTLEY EDUTECH**

Department of Electrical & Electronics Engineering

WINNERS- WOMAN HACKATHON

IEEE WIE AG and IEDC of Vimal Jyothi Engineering college in association with Tinkerhub has conducted an overnight hackathon 'TINK-HER+HACK 3.0' on 1st and 2nd February 2025 at Vimal Jyothi Engineering College. The event aimed to encourage the women students to explore their interest in technology and innovation through a hackathon style competition. Here are the winners.. Mrs. Sivendu, Neha and Sreenanda of S4 EEE







S3 UNIVERSITY TOPPERS

DEPARTMENT OF ENGINEERING OF ENGINEE



2nd Sivendu M. SGPA: 8.14



1st Saniya Joseph SGPA: 8.64



VIMAL JYOT

3rd Swejith SGPA: 7.32

Congratulations....

S5 UNIVERSITY TOPPERS



VIMAL JYOTHI ENGINEERING COLLEGE (AUTONOMOUS) DEPARTMENT OF EEE



Congratulations

TO S5 UNIVERSITY TOPPERS



JERIN BIJU SGPA 8.96





ELIZABETH T MANI SGPA 8.65



THOMAS MANOJ CHACKO SGPA 8.63

MASTERCLASS- S4,S6 CLASSES

Electrical and Electronics Engineering department of Vimal Jyothi Engineering College successfully organized a "Master class by UNSW Sydney" on March 20, 2025 for S4 and S6 students. The event was organized in collaboration with Placement cell VJEC and ISTEP mentors which aimed to provide students with a deep understanding of how creativity and scientific principles combine to drive business innovation.





PLACEMENT ASSISTANCE- TALK ON BIOMEDICAL ENGINEERING

As a part of the placement training, S6 students of Electrical and Electronics Engineering Department had a training session on 3rd March 2025 by the Leeway Biomedical a company from Bangalore. The session was inaugurated by HOD Prof. Laly James.





STUDENT ACHIEVEMENT-GATE QUALIFIED







Affiliated to APJ Abdul Kalam Technological University | Approved by AICTE Accredited by Institution of Engineers(India)

DEPARTMENT OF

ELECTRICAL & ELECTRONICS ENGINEERING



GATE 2025 QUALIFIED





ALUMNI ACHIEVEMENTS

A startup by 2014-18 EEE batch alumni Mr. Robin K Thomas and Jithin Vidya Ajith "Astrek innovations" (a leading developer of exoskeleton technology) got Youth Startup icon award in youth startup festival and honoured by Hon. Chief Minister Sri. Pinarai Vijayan.



Mr. Robin K thomas



Mr. Jithin Vidya Ajith





The startup also got DRDO best startup project award.

It was selected for OIST innovation accelerator programme in Japan. Got 58 lakh Japan grant.

PROGRAM EDUCATIONAL OBJECTIVES (PEOS)

- 1. GRADUATES WILL ACHIEVE BROAD AND IN-DEPTH KNOWLEDGE OF ELECTRICAL & ELECTRONICS ENGINEERING RELATING TO INDUSTRIAL PRACTICES AND RESEARCH TO ANALYZE THE PRACTICAL PROBLEMS AND THINK CREATIVELY TO GENERATE INNOVATIVE SOLUTIONS USING APPROPRIATE TECHNOLOGIES.
- 2. GRADUATES WILL MAKE VALID JUDGMENT, SYNTHESIZE INFORMATION FROM A RANGE OF SOURCES AND COMMUNICATE THEM IN SOUND WAYS APPROPRIATE TO THE DISCIPLINE.
- 3. GRADUATES WILL SUSTAIN INTELLECTUAL CURIOSITY AND PURSUE LIFELONG

LEARNING NOT ONLY IN AREAS THAT ARE RELEVANT TO ELECTRICAL & ELECTRONICS ENGINEERING, BUT ALSO THAT ARE IMPORTANT TO SOCIETY 4. GRADUATES WILL ADAPT TO DIFFERENT ROLES AND DEMONSTRATE LEADERSHIPS IN GLOBAL WORKING ENVIRONMENT BY RESPECTING DIVERSITY, PROFESSIONALISM AND ETHICAL PRACTICES

PROGRAM SPECIFIC OUTCOMES (PSOS)

APPLY THE KNOWLEDGE OF ELECTRICAL FUNDAMENTALS, CIRCUIT DESIGN, CONTROL ENGINEERING, ANALOG & DIGITAL ELECTRONICS TO THE FIELD OF ELECTRICAL & ELECTRONICS SYSTEMS IN INDUSTRY.

DEVELOP TECHNICAL KNOWLEDGE, SKILL, AND COMPETENCE TO IDENTIFY COMPREHEND AND SOLVE PROBLEMS IN RESEARCH AND ACADEMIC RELATED TO POWER SYSTEM ENGINEERING, INDUSTRIAL DRIVES & CONTROL.



PROGRAM OUTCOMES (POS)

- 1.Engineering Knowledge
- 2.Problem Analysis
- 3.Design/ Development of Solutions
- 4.Conduct Investigations of Complex
- Problems
- 5.Modern Tool Usage
- 6.The Engineer and Society
- 7.Ethics
- 8. Individual and Team Work
- 9.Communication
- 10.Project Management and Finance
- 11.Life-long learning



HAPPY EASTER AND HAPPY VISHU TO ALL

STAFF EDITOR RIJOY G N

