NEWS LETTER ELECTRICAL GNOSYS

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VISION

To evolve as a centre of excellence, to train students in contemporary technologies, to meet the needs of global industry and to develop them into skillful engineers instilled with human values and professional ethics.

MISSION

To produce competent and disciplined Electrical & Electronics Engineers through delivery of quality education to meet the ongoing global challenges in alignment with technical education system and society.

INSIDE THE ISSUE

"Technology like art is a soaring exercise of the human imagination." – Daniel Bell

HOD'S DESK

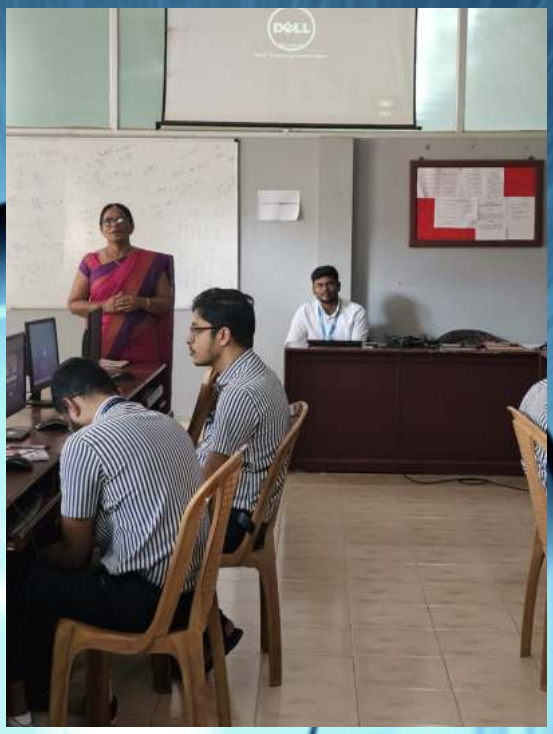
Prof. Laly James

I am glad to note that the Department of Electrical and Electronics Engineering is ready to release another edition of Newsletter "ELECTRICAL GNOSYS". I appreciate the efforts of staff's and student's for past month and here are some of the highlights of events in past month.

It is a proud moment for all of us that Vimal Jyothi Engineering College (VJEC) was conferred autonomous status by the University Grants Commission (UGC) of India!

This is a remarkable milestone in the college's journey

Placement orientation- S8 students



Placement training for S8 students- By Wartens, Banglore. Sessions handled by Mr. Harivishwa R, Mr. JishnuRaj and Mr. Elttin Joy


RESEARCH
WEBINAR
Introduction to Research Methodologies
MONDAY
11:30 am- 12:30 pm
12 th February 2024
SPEAKER
Dr. Noby George
Systems Engineer
Allegro Microsystems
Czech Republic

Value added Programme- S6 students

The Department of Electrical and Electronics Engineering, in association with Revertech, organised a value-added course on artificial intelligence and machine learning for S6 EEE students from February 21 to February 25, 2024, at the AEI Research Lab. The aim is to provide participants with comprehensive insights into Python programming, Arduino electronics, and the fundamentals of machine learning.

Congratulations- S6 toppers

First topper- Ashwanth Shaji
Second topper-Ashwanth K M
Third topper- Vineeth Binoy

Congratulations-S4 toppers

First topper- JERIN BIJU
Second topper-DITHU PRASANTH
Third topper 1- THOMAS MANOJ CHACKO
Third topper 2- ELIZABETH T MANI


VIMAL JYOTHI
ENGINEERING COLLEGE
IEEE Women in Engineering
WIE
IEEE
Invest in Women: Accelerate Progress
International Women's Day
with
Prof. Elizabeth Rita Samuel
Associate professor, Rajagiri School of Engineering and technology
Secretary, WIE Kerala Section
08 March, 2024 | 7 PM
For Registration

Distinguished lecture Program on Autonomous ground vehicles and Intelligent transportation system.

The session was handled by Dr. Christina Olaverri-Monreal, HoD Intelligent Transportation Systems, Johannes kepler University Linz, Austria.

Mr. Pranav T V
2019-23 batch

Ms. Sredha Alex
2019-23 batch

Sent off- Rojith sir

The Department bid farewell to Mr. Rojith K, Asst. Professor, extending heartfelt gratitude for his significant contributions. He relieved from the institution to pursue his PhD.

Faculty achievement

Prof. Laly James attended FDP on councelling and academic mentoring organized by the Human Resource Development centre (HRDC) of KTU at Vimal Jyothi Engineering College from 27.02.2024 to 02.03.2024

Ms. Athira Tomas, Ms. Tintu George and Ms. Keerthana T V Attended 5 days online FDP on the topic"Opportunities on Power Electronics for Smart Grid, Renewable Energy and EV" organised by SCMS

PROGRAM EDUCATIONAL OBJECTIVES (PEOS)

-GRADUATES WILL ACHIEVE BROAD AND IN-DEPTH KNOWLEDGE OF ELECTRICAL & ELECTRONICS ENGINEERING RELATING TO INDUSTRIAL PRACTICES AND RESEARCH TO ANALYZE THE PRACTICAL PROBLEMS AND THINK CREATIVELY TO GENERATE INNOVATIVE SOLUTIONS USING APPROPRIATE TECHNOLOGIES.
-GRADUATES WILL MAKE VALID JUDGMENT, SYNTHESIZE INFORMATION FROM A RANGE OF SOURCES AND COMMUNICATE THEM IN SOUND WAYS APPROPRIATE TO THE DISCIPLINE.
-GRADUATES WILL SUSTAIN INTELLECTUAL CURIOSITY AND PURSUE LIFELONG LEARNING NOT ONLY IN AREAS THAT ARE RELEVANT TO ELECTRICAL & ELECTRONICS ENGINEERING, BUT ALSO THAT ARE IMPORTANT TO SOCIETY
-GRADUATES WILL ADAPT TO DIFFERENT ROLES AND DEMONSTRATE LEADERSHIPS IN GLOBAL WORKING ENVIRONMENT BY RESPECTING DIVERSITY, PROFESSIONALISM AND ETHICAL PRACTICES

PROGRAM SPECIFIC OUTCOMES (PSOS)

APPLY THE KNOWLEDGE OF ELECTRICAL FUNDAMENTALS , CIRCUIT DESIGN, CONTROL ENGINEERING, ANALOG & DIGITAL ELECTRONICS TO THE FIELD OF ELECTRICAL & ELECTRONICS SYSTEMS IN INDUSTRY.
DEVELOP TECHNICAL KNOWLEDGE, SKILL, AND COMPETENCE TO IDENTIFY COMPREHEND AND SOLVE PROBLEMS IN RESEARCH AND ACADEMIC RELATED TO POWER SYSTEM ENGINEERING, INDUSTRIAL DRIVES & CONTROL.

PROGRAM OUTCOMES (POS)

1.ENGINEERING KNOWLEDGE
2.PROBLEM ANALYSIS
3.DESIGN/ DEVELOPMENT OF SOLUTIONS
4.CONDUCT INVESTIGATIONS OF COMPLEX PROBLEMS
5.MODERN TOOL USAGE
6.THE ENGINEER AND SOCIETY
7.ENVIRONMENT AND SUSTAINABILITY
8.ETHICS
9.INDIVIDUAL AND TEAM WORK
10.COMMUNICATION
11.PROJECT MANAGEMENT AND FINANCE
12.LIFE-LONG LEARNING

All the best Rojit sir for your future endevoures

EDITORS

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