VIMAL JYOTHI
ENGINEERING COLLEGE (AUTONOMOUS)
Jyothi Nagar, Chemperi - 670632, Kannur D.T, Kerala

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

BOARD OF STUDIES (BoS) MEETING MINUTES

Date: 22-10-2024

Time: 2:00 PM

Mode of Meeting: Online (https://meet.google.com/zbj-ucwg-hrz)

Agenda:

1. Review and approval of B-Tech ECE Semester I to VIII Curriculum of Autonomous batch starting from 2024-25 academic year
2. Review and approval of B-Tech ECE first year syllabus Autonomous batch starting from 2024-25 academic year

Members Present:

1. Dr. Anto Sahaya Dhas, HOD ECE
2. Dr. Madheswaran M Principal Muthayammal Engineering College, Academic Council Nominee
3. Dr. Kamalraj Subramaniam, Professor and Head, BME,Karpagam Academy of Higher Education, Academic Council Nominee
4. Dr. C N Anilkumar, Director, EHS & Sustainability, Schneider Electric Mumbai, Industry Nominee
5. Ms. Amrutha Nanukuttan, Physical Design Engineer, Intel, Alumni
6. Dr. Roshini T V, Professor, ECE
7. Ms. Jerrin Yomas, Asso. Prof, ECE
8. Ms. Shimna P K Asst Prof, ECE
9. Ms. Sudharsana Vijayan, Asst Prof, ECE
10. Mr. Binil Kumar K , Asst Prof, ECE
11. Ms. Bindu Sebastian, Asst Prof, ECE
12. Ms. Grace Jhon, Asst Prof, ECE
13. Ms. Anusha Chacko, Asst Prof, ECE
14. Mr. Vinod J Thomas, Asst Prof, ECE
15. Mr. Manoj K C, Asst Prof, ECE
16. Ms. Ann Mathew, Asst Prof, ECE

Proceedings of the Meeting:

The first BoS meeting for the department of Electronics and Communication Engineering was held on 22-10-2024 at 2:00 PM in online mode.

The Chairperson, Dr. Anto Sahaya Dhas HOD ECE, welcomed all the members of the BoS. The Chairperson briefed the members about the objective of the meeting, focusing on the revision and approval of the curriculum and syllabus for the autonomous batch starting from 2024-25 academic years

Discussions:

- It was suggested that the curriculum for the first year may be aligned with the university syllabus as already prepared by the syllabus review committee.
- The committee agreed that this approach would ensure consistency and adherence to the university guidelines for the first year.
- The members proposed that from the third semester onwards, the syllabus should be prepared with the autonomous perspective.
- This would allow for more flexibility and tailoring of the content to meet the specific needs of the institution, and its students.
- A key suggestion was to include industry-oriented subjects from the lower semesters itself.
- This would help in equipping students with practical, job-related skills early in their education, making them more competitive in the job market.
- It was decided that sub-committees would be formed to handle the creation and review of the syllabus for semesters 3 to 8, with a focus on integrating industry relevance.

Action Taken Report

Sl No	Points discussed	Action Points	Decisions
1	Review of Proposed B.Tech ECE Curriculum (Autonomous)	Follow the Curriculum already prepared for the first year. The remaining semesters curriculum needs change and will be considered in the next meeting	Only First Year Curriculum and Syllabus approved.
2	Reviewed the changes incorporated in the subject "GXEST203 Foundations of Computing: From Hardware Essentials to Web Design"	Number Systems was incorporated into Module 2. 'Boot Process' was moved from Module 1 to Module 3, and 'Switches' was included in Module 3.	Approved.
3	Reviewed the changes incorporated in the subject "GXESL208 IT Workshop"	"Familiarization of BIOS" was added to the second experiment and "crimping" to the fifth experiment.	Approved.
4	Reviewed the changes incorporated in the subject GXESL106 Basic Electrical and Electronics Engineering Workshop	Added a new experiment "Familiarization of Arduino and implementation of simple circuits"	Approved.

5	Reviewed the changes incorporated in the subject "PCECT205 Network Theory"	• Millman's theorem in module 2 is omitted because the functionality provided by Millman's theorem can also be achieved using mesh/node analysis or other network theorems. • In module 2 Transient analysis of RL, RC and RLC networks with DC, impulse, step and sinusoidal inputs is corrected as Transient analysis of RL, RC and RLC networks with DC, impulse, step and sinusoidal inputs **using Laplace transform** in order **to** avoid any confusion with time domain methods. • Analysis of low pass and high pass RC circuits using Laplace transforms in module 3 is omitted due to these circuits are indeed types of RC circuits, their analysis using Laplace transforms are already covered in the same module.	Approved